

**Toyohashi University of Technology**  
**Recruitment of Department of Electrical and Electronic Information Engineering Faculty Members**  
**(Professors)**

Recruitment Professor 1 position

Affiliated Fields Integrated Electronics Field

Fields of Specialty Design, fabrication, and evaluation of integrated circuits (integrated circuits for the IoT that transmit and process information acquired by various sensors, and next-generation integrated circuits that realize functions such as artificial intelligence and deep learning) (experimental field)

Responsible Subjects Subjects concerning Electrical and Electronic Information Engineering

Qualifications

Persons, who meet all of the following requirements, \*

- (1) must have a doctorate or Ph.D. and possess advanced research experience in her field.
- (2) must be able to conduct lectures concerning Electrical and Electronic Information Engineering and have strong enthusiasm with regards to guiding the educational research of students \*\*
- (3) must maintain design and production facilities of integrated circuits with enthusiasm, and be able to do education and research.
- (4) if not a native speaker of Japanese, must have Japanese language ability to be engaged in office work.

\*In consideration of gender equality, we are proactively promoting the hiring of female faculty members.

\*\*In order to advance the globalization of our educational research, we would like to hire faculty members who are capable of conducting lectures and providing guidance to students in English.

Planned Hiring Date As early as possible after April 1, 2024

Compensation

- (1) Salary: Based on the Pay Regulations for University Faculty of the Toyohashi University of Technology
- (2) Other benefits: Dependency allowance, housing allowance, commutation allowance, transfer allowance, etc.  
(Provided upon confirming requirements)
- (3) Work hours: Flexible time system based around the hours of 8:30 to 17:15
- (4) Holidays: Saturdays, Sundays, national holidays, New Year's holidays and designated summer holidays
- (5) Insurance: The Ministry of Education, Culture, Sports, Science and Technology Mutual Aid Association Insurance (short-term), and the Employees' Pension Insurance (long-term)
- (6) Other: No smoking on campus

Employment System

Full-time employment

Term: Open-ended (Unrestricted)\*

Documents to Submit\*

- (1) Resume (Can be printed on standard, commercially available resume forms. Must include a photo and your e-mail address.)
- (2) List of research achievements (Classified into the following categories: literary works, peer-reviewed papers, international conference papers, reviews/commentaries, invited lectures/presentations, patents, doctoral thesis, etc.)

- (3) Printouts of three major articles (Copies are acceptable.)
  - (4) Status of activities at institutes and in academic societies (status of activities at affiliated institutes or other institutes, awards, practical research, overseas activities other than international conferences, and other items that demonstrate your qualifications.)
  - (5) Status of acquisition of external funding (Specify, by representative or contributor, according to the following: national/ministry research projects, Grants-in-Aid for Scientific Research, academic-industrial collaboration projects, foundation grants, internal competitive funds in an organization, etc. Specify other items that demonstrate your qualifications.)
  - (6) Overview of past research (A4 size, within two pages) and plans for future research (A4 size, within two pages)
  - (7) Experience and aspirations regarding education (A4 size, within two pages)
  - (8) Contact information of three references (two from Japan and one from overseas: name, affiliation, position, telephone number, e-mail address)
- \*You may be asked to submit other documents if the selection conditions require it.

#### Selection Method

- (1) Document examination (Selection will be made according to the selection standards of our university)
- (2) Interview: Applicants determined to be suitable after the document examination phase will be contacted individually for an interview.\*

\*We do not respond to individual inquiries regarding selection results.

Application Deadline November 30, 2023 (date by which documents must arrive)

#### Address to Mail the Documents\*

Department Chair

Department of Electrical and Electronic Information Engineering, Toyohashi University of Technology  
1-1 Hibarigaoka, Tempaku, Toyohashi, Aichi, 4418580

\*“Documents for Application for Faculty Member in the Integrated Electronics Field (Professors) Enclosed” must be written in red ink on the envelope, and the documents must be delivered by registered mail.

Contact Professor Yasuhiko Isikawa

Tel: +81-532-44-6741 E-mail: ishikawa@ee.tut.ac.jp

#### Other Items

- (1) We do not return application documents without request.
- (2) Based on the Personal Information Protection Act, personal information contained in the application documents shall not be used for any purpose other than selection.
- (3) Costs for coming to the university, such as attending the interview, shall be borne by the applicant.
- (4) Applicants shall be notified of selection results following the full examination period.