

Toyohashi University of Technology
Recruitment of Department of Electrical and Electronic Information Engineering Faculty Members
(Assistant Professors)

Recruitment Assistant Professor 1 position

Affiliated Fields Integrated Electronics

Fields of Specialty Semiconductor optical devices, Integrated photonic devices, Silicon photonics
(Experiments, device fabrication)

Qualifications*

Persons, who meet all of the following requirements,

- (1) must have a doctorate or Ph.D. or must be expecting to obtain one by time of hiring, and must have research experience in his/her field.
- (2) must be able to conduct student experiments related to electrical and electronic information engineering.
- (3) must have strong enthusiasm and creativity for assisting and guiding the educational research of students (including international students).
- (4) if not a native speaker of Japanese, must have Japanese language ability to be engaged in office work.

*In consideration of gender equality, we are proactively promoting the hiring of female faculty members.

Planned Hiring Date April 1, 2021

Compensation

- (1) Salary: Based on the Pay Regulations for University Faculty of the Toyohashi University of Technology (Annual salary system)
- (2) Other benefits: Dependency allowance, housing allowance, commutation allowance, transfer allowance, etc.
(Provided upon confirming requirements)
- (3) Work hours: Flexible time system based around the hours of 8:30 to 17:15
- (4) Holidays: Saturdays, Sundays, national holidays, New Year's holidays, and designated summer holidays
- (5) Insurance: The Ministry of Education, Culture, Sports, Science and Technology Mutual Aid Association Insurance (short-term), and the Employees' Pension Insurance (long-term)

Employment System Full-time employment (with term)*

*Term: Five years (you may be applied newly tenure-track system that is planning to establish)

Documents to Submit

- (1) Resume (Can be printed on standard, commercially available resume forms. Must include a photo and your e-mail address.)
- (2) List of research achievements (Classified into the following categories: literary works, peer-reviewed papers, international conference essays, domestic conference essays, reviews/commentaries, patents, dissertation, etc.)
- (3) Printouts of three major articles (Copies are acceptable.)
- (4) Status of activities at institutes and in academic societies (status of activities at affiliated institutes or other institutes, awards, invited lectures at international conferences, domestic conferences, symposiums, etc., practical research, overseas activities other than international conferences, and other items that demonstrate your qualifications.)
- (5) Status of acquisition of external funding (Specify, by representative or contributor, according to the

following: Grants-in-Aid for Scientific Research, academic-industrial collaboration projects, foundation grants, internal competitive funds in an organization, etc. Specify other items that demonstrate your qualifications.)

(6) Overview of past research (A4 size, within two pages) and plans for future research (A4 size, within two pages)

(7) Experience and aspirations regarding education (A4 size, within two pages)

(8) Contact information of two references (name, affiliation, position, telephone number, e-mail address)*

*You may be asked to submit other documents if the selection conditions require it.

Selection Method

(1) Document examination (Selection will be made according to the selection standards of our university)

(2) Interview: Applicants determined to be suitable after the document examination phase will be contacted individually for an interview.*

*We do not respond to individual inquiries regarding selection results.

Application Deadline November 6, 2020 (date by which documents must arrive)

Address to Mail the Documents*

〒441-8580 1-1 Hibarigaoka, Tempaku, Toyohashi, Aichi, Japan

Chair of Department of Electrical and Electronic Information Engineering, Toyohashi University of Technology

*“Documents for Application for Faculty Member (Assistant Professors) in the Integrated Electronics Field Enclosed” must be written in red ink on the envelope, and documents must be delivered by registered mail.

Contact

Department of Electrical and Electronic Information Engineering, Toyohashi University of Technology

Professor Yasuhiko ISHIKAWA

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Other Items

(1) We do not return application documents without request. If you would like the documents to be returned, please enclose a return envelope with your documents (with a stamp or delivery payment invoice affixed to it.)

(2) Based on the Personal Information Protection Act, personal information contained in the application documents shall not be used for any purpose other than selection.

(3) Costs for coming to the university, such as to attending the interview, shall be borne by the applicant.

(4) Applicants shall be notified of selection results following the full examination period.